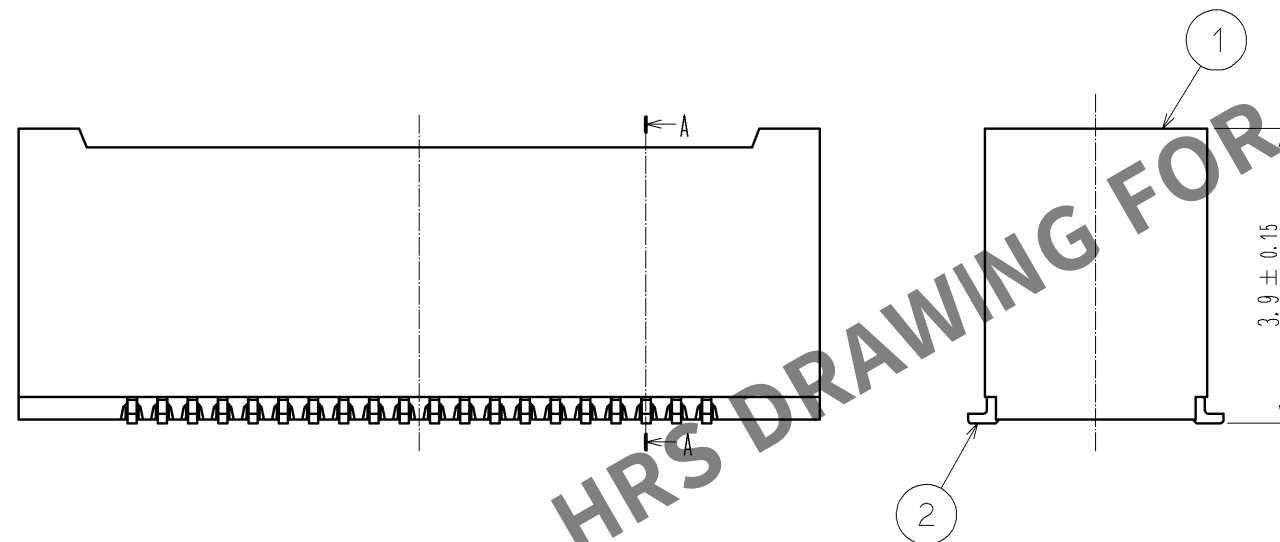
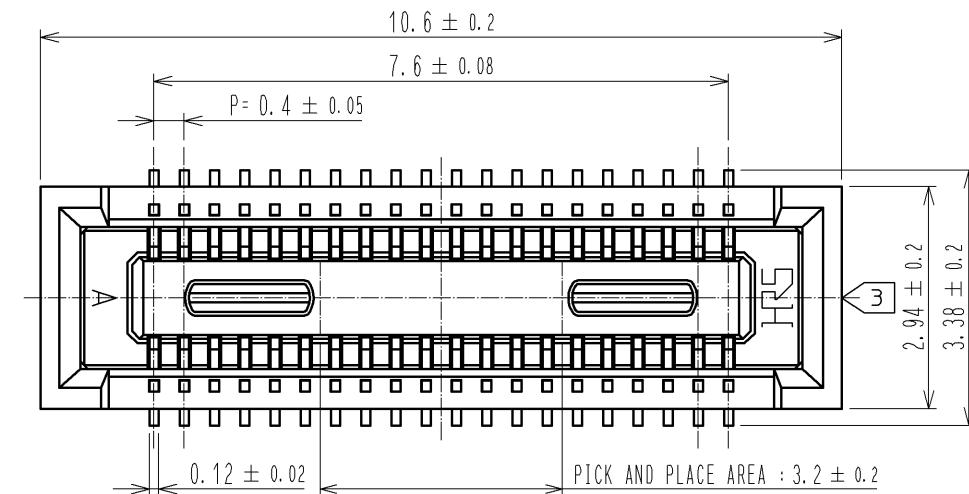
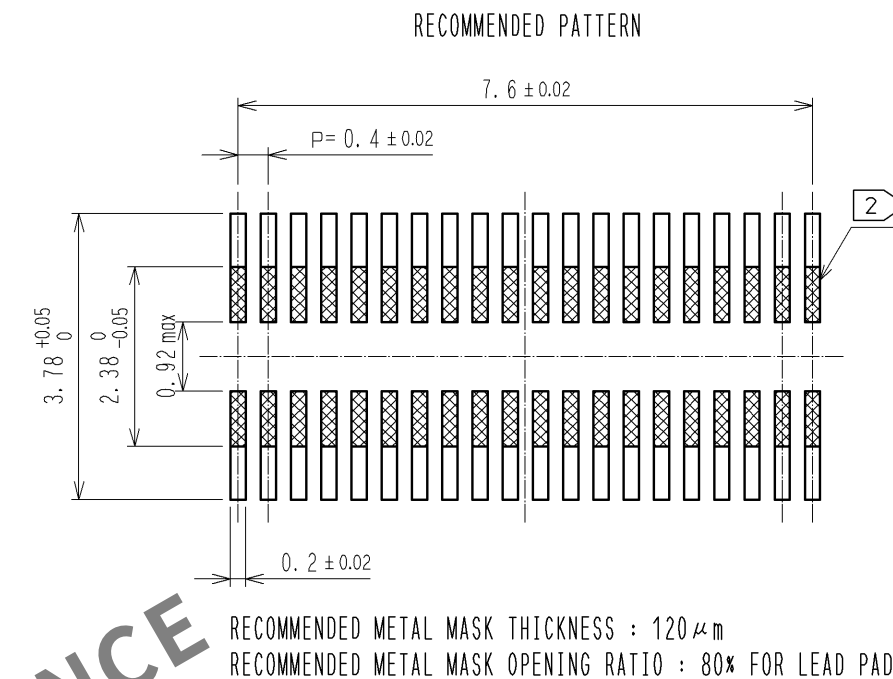
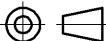




NOTE 1. ALL LEADS CO-PLANARITY SHALL BE 0.1 MAX.
2 IS NO PATTERN AREA OTHER THAN THE SAME CIRCUIT.
3 GATE POSITION IS POSITIONED IN APPROXIMAL AREA SHOWN.



2	PHOSPHOR BRONZE	CONTACT AREA:GOLD 0.05μm MIN						
		SMT LEAD:GOLD 0.05μm MIN	5	PS	BLACK			
		UNDERPLATING:NICKEL 1μm MIN	4	POLYESTER	CLEAR, COVER TAPE			
1	LCP	BLACK, UL94V-0	3	PS	CLEAR, EMBOSSED CARRIER TAPE			
NO.	MATERIAL	FINISH , REMARKS	NO.	MATERIAL	FINISH , REMARKS			
UNITS mm		SCALE 10 : 1		COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
 HIROSE ELECTRIC CO., LTD.	APPROVED : KH. IKEDA		10.03.31		DRAWING NO. EDC3-319007-01			
	CHECKED : AR. TAKAHASHI		10.03.31		PART NO. DF40HC(4.0)-40DS-0.4V(70)			
	DESIGNED : TK. SUZUKI		10.03.30		CODE NO. CL684-4111-6-70			
	DRAWN : TK. SUZUKI		10.03.30					

