RATING		DARD			T		1		
_	OPERATING TEMPERATURE RANGE VOLTAGE CURRENT		-55 °C TO 125 °C (NO	DTES 1)	STORAGE TEMPERATU	IRE RANGE	-10 °C TO 60 °C (NO	TES 2	2)
			50 V AC						
	UKKENI		0.3 A						
					IONS			1	1
ITE			TEST METHOD			REQL	JIREMENTS	QT	A
CONSTRU								X	
GENERAL EXAMINATION		VISUALLY AND BY MEASURING INSTRUMENT.			ACCO	ACCORDING TO DRAWING.			2
MARKING		CONFIRMED VISUALLY.						Х	2
	C CHARA								
		20 mV AC OR LESS 1 kHz, 1 mA.			50 mΩ	MAX.		Х	-
NSULATION RESISTANCE		100 V DC			500 M	500 MΩ MAX		Х	-
VOLTAGE PROOF		150 V AC FOR 1 min.			NO FL	ASHOVER	OR BREAKDOWN.	Х	
MECHANIC	CAL CHAR	ACTERI	STICS						
VIBRATION		50 TIMES INSERTIONS AND WITHDRAWALS.			① CO	① CONTACT RESISTANCE: 50 m Ω MAX.			-
						② NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE			00		AL DISCONTINUITY OF 1 μ s.	Х	-
		0.75 mm, AT 2 h, FOR 3 DIRECTIONS.				2 NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
		490 m/s ² DURATION OF PULSE 11 ms AT 3 TIMES FOR 3 DIRECTIONS.				 NO ELECTRICAL DISCONTINUITY OF 1 μs. NO DAMAGE, CRACK AND LOOSENESS OF PARTS. 			-
			TERISTICS		Z NO I	JAMAGE, CRA	CK AND LOUSENESS OF PARTS.		
RAPID CHAN			TURE -65 →15 TO 35 →125	5 →15 TO 35	°C ① CON	ITACT RESIS	STANCE: 50 mΩ MAX.	Х	Γ.
TEMPERATURE		TIME $30 \rightarrow 2 \text{ TO } 3 \rightarrow 30 \rightarrow 2 \text{ TO } 3 \text{ min}$			-	(2) INSULATION RESISTANCE: 500 M Ω MIN.			
		UNDER 5 CYCLES.			-	③ NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
DAMP HEAT (STEADY STATE)		EXPOSED AT 40 ± 2 °C, 90 TO 95 %, 96 h.			-	(1) CONTACT RESISTANCE: 50 m Ω MAX. (2) INSULATION RESISTANCE: 500 M Ω MIN.			
					-	INSULATION RESISTANCE: 500 M2 MIN. INSULATION RESISTANCE: 500 M2 MIN. INSULATION RESISTANCE: 500 M2 MIN.			
SULPHUR DIOXIDE		EXPOSED IN 25 PPM RH 75 % FOR 96 h.			÷	1 CONTACT RESISTANCE: 50 m Ω MAX.			
HEAT RESISTANCE OF			ANDARD:JEIDA-38) MENDED TEMPERATURE		-	HEAVY COR	ROSION. OF CASE OF EXCESSIVE	X	
SOLDERING		 (SOLDERING AREA) MAX250°C, 220°C FOR 60 SECONDS MAX. (PREHEATING AREA) 150 TO 180°C 90~120 SECONDS. MAXIMUM TWICE ACTION IS ALLOWED UNDER THE SAME CONDITION. [RECOMMENDED MANUAL SOLDELING CONDITION] SOLDERING IRON TEMPERATURE 350°C SOLDERING TIME : WITHIN 3 SECONDS. 			ΉE		IE TERMINALS.		
NOTES2:STOR	RAGEIS DEFINE	ED AS LONG	RE RISE BY CURRENT. 3-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE		-	ER SUPLLY	·.		
NOTES1:INCLU NOTES2:STOR APPLY OPERA	AGEIS DEFINE	ED AS LONG	G-TERM STORAGE OF UNUSE		-	/ER SUPLLY	·		
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE	ED ON PCB W	-	/ER SUPLLY	CHECKED	DA	TE
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW		CHECKED	DA	(TE
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW	APPROVE	CHECKED	DA 2020	
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW		CHECKED D WR. FUKUCHI		07
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW	APPROVE	CHECKED D WR. FUKUCHI D TS. MIYAZAKI	2020	07 07
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	AGEIS DEFINE TION TEMPER	ED AS LONG ATURE RAI FIED , REFE	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW	APPROVE CHECKE	CHECKED D WR. FUKUCHI D TS. MIYAZAKI D KT. KUSAKA	2020 2020	07 07
NOTES1:INCLU NOTES2:STOR NPPLY OPERA JNLESS OTHE COUNT	RAGEIS DEFINE TION TEMPER RWISE SPECI	ED AS LONG	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 .	ED ON PCB W	ITHOUT POW	APPROVE CHECKEI DESIGNE DRAWN	CHECKED D WR. FUKUCHI D TS. MIYAZAKI D KT. KUSAKA	2020 2020 2020 2020	07 07 07
NOTES1:INCLU NOTES2:STOR APPLY OPERA JNLESS OTHE COUNT	RAGEIS DEFINE TION TEMPER RWISE SPECI DE	ED AS LONG ATURE RAI FIED , REFE ESCRIPTIC	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 . ON OF REVISIONS	ED ON PCB W	ESIGNED	APPROVE CHECKEI DESIGNE DRAWN IG NO.	CHECKED D WR. FUKUCHI D TS. MIYAZAKI D KT. KUSAKA RN. I IDA	2020 2020 2020 2020 2020 1-0 1	07 07 07
NOTES1:INCLU NOTES2:STOR NPPLY OPERA JNLESS OTHE COUNT	RAGEIS DEFINE TION TEMPER RWISE SPECI DE Lalification Te	ED AS LONG ATURE RAI FIED , REFE ESCRIPTIC	G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402 . ON OF REVISIONS	Test	ESIGNED	APPROVE CHECKEI DESIGNE DRAWN IG NO. DF1	CHECKED D WR. FUKUCHI D TS. MIYAZAKI D KT. KUSAKA RN. I IDA ELC-389326-5 2NB (5. 0) -60DP-0. 5V	2020 2020 2020 2020 1-01 (51)	0. 0.