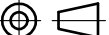


Table.2 Pin configuration of Interposer  4

S: Signal for Differential SE: Signal for Single-ended G1: Common ground 1 G2: Common ground 2

3	Copper Alloy	Ni1.5 μ m+Au0.76 μ m	6	PS	(Tray)		
2	LCP(Black)	UL94V-0	5	Copper Alloy	Ni1 μ m		
1	LCP(Gray)	UL94V-0	4	Copper Alloy	Ni1 μ m		
NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS		
UNITS mm		SCALE 2 : 1	COUNT  9	DESCRIPTION OF REVISIONS DIS-F-00002513	DESIGNED AS. MATSUZAWA	CHECKED MK. EZAKI	DATE 17. 07. 14
 HIROSE ELECTRIC CO., LTD.	APPROVED : MK. EZAKI	16. 02. 25	DRAWING NO.	EDC-355706-03-03			
	CHECKED : MK. EZAKI	16. 02. 25	PART NO.	FX10-144IP-44Q-8PH(03)			
	DESIGNED : TS. OSHIDA	16. 02. 25	CODE NO.	CL608-0010-4-03			
	DRAWN : TS. OSHIDA	16. 02. 25					

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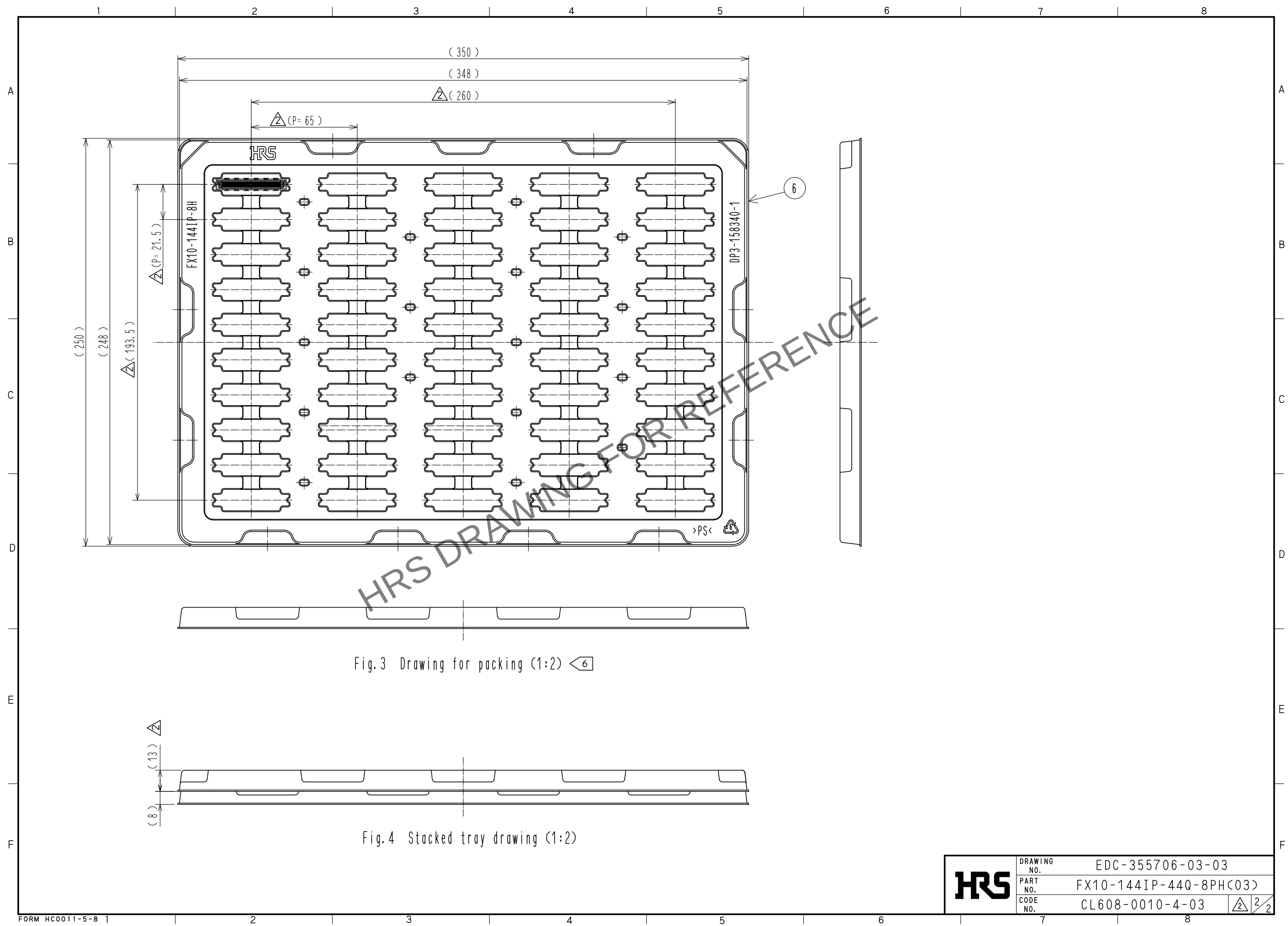


Fig.3 Drawing for packing (1:2) 6

Fig.4 Stacked tray drawing (1:2)

HRS	DRAWING NO.	EDC-355706-03-03		
	PART NO.	FX10-144IP-44Q-8PH(03)		
	CODE NO.	CL608-0010-4-03	2	2