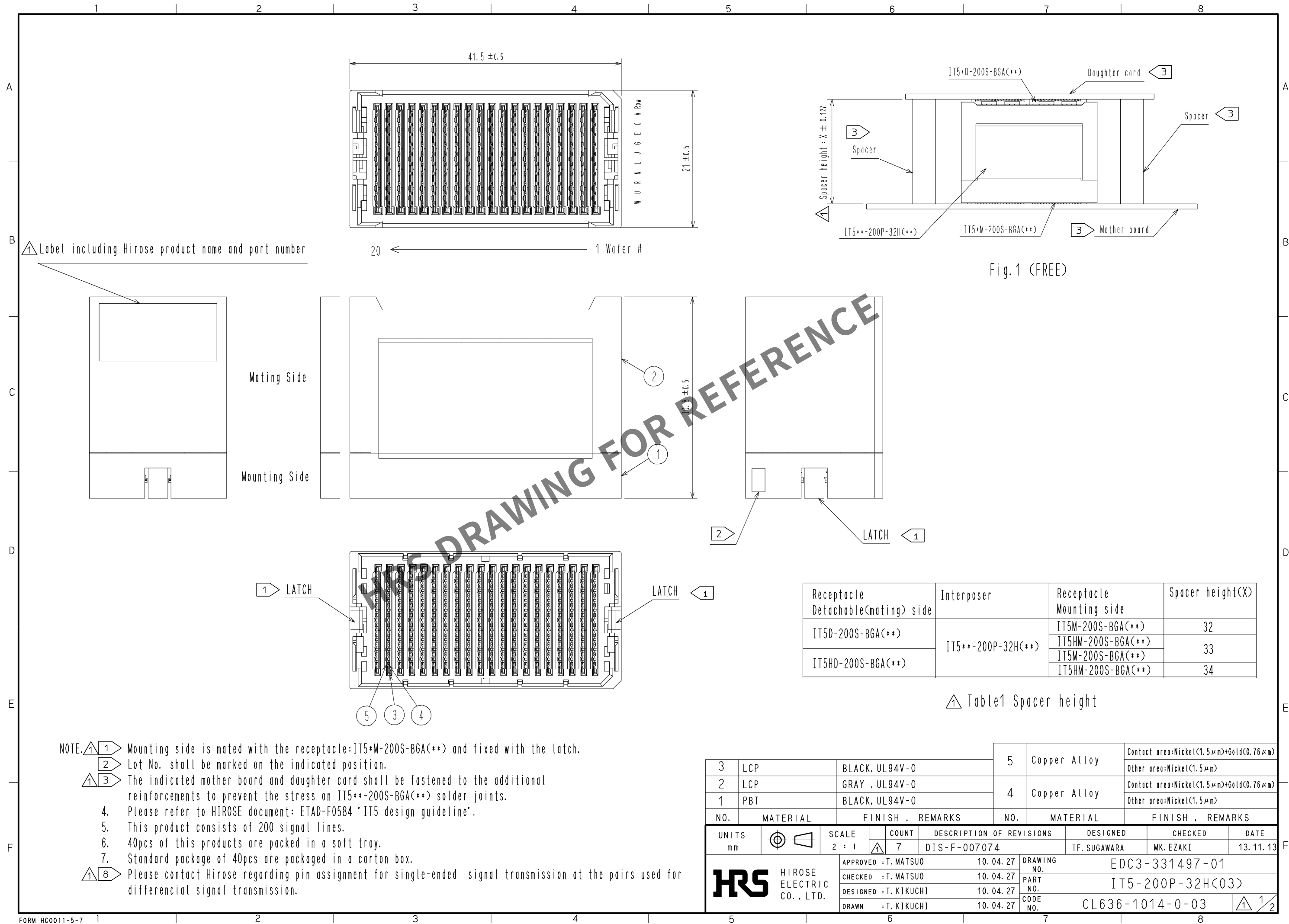




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Pin connection
Wafer even #
(Wafer A of IT5)

Wafer type		Wafer #	
		Column	Row
IT5 wafer B (100 ohm)	20	40	X
IT5 wafer B (100 ohm)	19	39	A
IT5 wafer A (100 ohm)	18	38	B
IT5 wafer A (100 ohm)	17	37	C
IT5 wafer B (100 ohm)	16	36	D
IT5 wafer B (100 ohm)	15	35	E
IT5 wafer A (100 ohm)	14	34	F
IT5 wafer A (100 ohm)	13	33	G
IT5 wafer B (100 ohm)	12	32	H
IT5 wafer B (100 ohm)	11	31	I
IT5 wafer A (100 ohm)	10	30	J
IT5 wafer A (100 ohm)	9	29	K
IT5 wafer B (100 ohm)	8	28	L
IT5 wafer B (100 ohm)	7	27	M
IT5 wafer A (100 ohm)	6	26	N
IT5 wafer A (100 ohm)	5	25	O
IT5 wafer B (100 ohm)	4	24	P
IT5 wafer B (100 ohm)	3	23	Q
IT5 wafer A (100 ohm)	2	22	R
IT5 wafer A (100 ohm)	1	21	S
IT5 wafer B (100 ohm)		20	T
IT5 wafer B (100 ohm)		19	U
IT5 wafer A (100 ohm)		18	V
IT5 wafer A (100 ohm)		17	W
IT5 wafer B (100 ohm)		16	X
IT5 wafer B (100 ohm)		15	Y
IT5 wafer A (100 ohm)		14	Z
IT5 wafer A (100 ohm)		13	AA
IT5 wafer B (100 ohm)		12	AB
IT5 wafer B (100 ohm)		11	AC
IT5 wafer A (100 ohm)		10	AD
IT5 wafer A (100 ohm)		9	AE
IT5 wafer B (100 ohm)		8	AF
IT5 wafer B (100 ohm)		7	AG
IT5 wafer A (100 ohm)		6	AH
IT5 wafer A (100 ohm)		5	AI
IT5 wafer B (100 ohm)		4	AJ
IT5 wafer B (100 ohm)		3	AK
IT5 wafer A (100 ohm)		2	AL
IT5 wafer A (100 ohm)		1	AM
IT5 wafer B (100 ohm)			AN
IT5 wafer B (100 ohm)			AO
IT5 wafer A (100 ohm)			AP
IT5 wafer A (100 ohm)			AQ
IT5 wafer B (100 ohm)			AR
IT5 wafer B (100 ohm)			AS
IT5 wafer A (100 ohm)			AT
IT5 wafer A (100 ohm)			AU
IT5 wafer B (100 ohm)			AV
IT5 wafer B (100 ohm)			AW
IT5 wafer A (100 ohm)			AX
IT5 wafer A (100 ohm)			AY

Pin connection
Wafer odd #
(Wafer B of IT5)

- △
Symbols
S: Signal (Differential, Single-ended, Power)
G: GND, Power
P: Positive of differential pair
N: Negative of differential pair
- 8  Twisted pair used for differential
- 8  Non twisted pair used for differential

IT5•M-200S-BGA(**) Pin assignment footprint for mounting side

Wafer type		Wafer #	
		Column	Row
IT5 wafer B (100 ohm)	20	40	Y
IT5 wafer B (100 ohm)	19	39	W
IT5 wafer A (100 ohm)	18	38	V
IT5 wafer A (100 ohm)	17	37	U
IT5 wafer B (100 ohm)	16	36	T
IT5 wafer B (100 ohm)	15	35	R
IT5 wafer A (100 ohm)	14	34	Q
IT5 wafer A (100 ohm)	13	33	P
IT5 wafer B (100 ohm)	12	32	N
IT5 wafer B (100 ohm)	11	31	M
IT5 wafer A (100 ohm)	10	30	L
IT5 wafer A (100 ohm)	9	29	K
IT5 wafer B (100 ohm)	8	28	J
IT5 wafer B (100 ohm)	7	27	I
IT5 wafer A (100 ohm)	6	26	H
IT5 wafer A (100 ohm)	5	25	G
IT5 wafer B (100 ohm)	4	24	F
IT5 wafer B (100 ohm)	3	23	E
IT5 wafer A (100 ohm)	2	22	D
IT5 wafer A (100 ohm)	1	21	C
IT5 wafer B (100 ohm)		20	B
IT5 wafer B (100 ohm)		19	A
IT5 wafer A (100 ohm)		18	X
IT5 wafer A (100 ohm)		17	W
IT5 wafer B (100 ohm)		16	V
IT5 wafer B (100 ohm)		15	U
IT5 wafer A (100 ohm)		14	T
IT5 wafer A (100 ohm)		13	R
IT5 wafer B (100 ohm)		12	Q
IT5 wafer B (100 ohm)		11	P
IT5 wafer A (100 ohm)		10	N
IT5 wafer A (100 ohm)		9	M
IT5 wafer B (100 ohm)		8	L
IT5 wafer B (100 ohm)		7	K
IT5 wafer A (100 ohm)		6	J
IT5 wafer A (100 ohm)		5	I
IT5 wafer B (100 ohm)		4	H
IT5 wafer B (100 ohm)		3	G
IT5 wafer A (100 ohm)		2	F
IT5 wafer A (100 ohm)		1	E
IT5 wafer B (100 ohm)			D
IT5 wafer B (100 ohm)			C
IT5 wafer A (100 ohm)			B
IT5 wafer A (100 ohm)			A
IT5 wafer B (100 ohm)			X

IT5•D-200S-BGA(**) Pin assignment footprint for mating side

HRS	DRAWING NO.	EDC3-331497-01
	PART NO.	IT5-200P-32H(C03)
	CODE NO.	CL636-1014-0-03

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