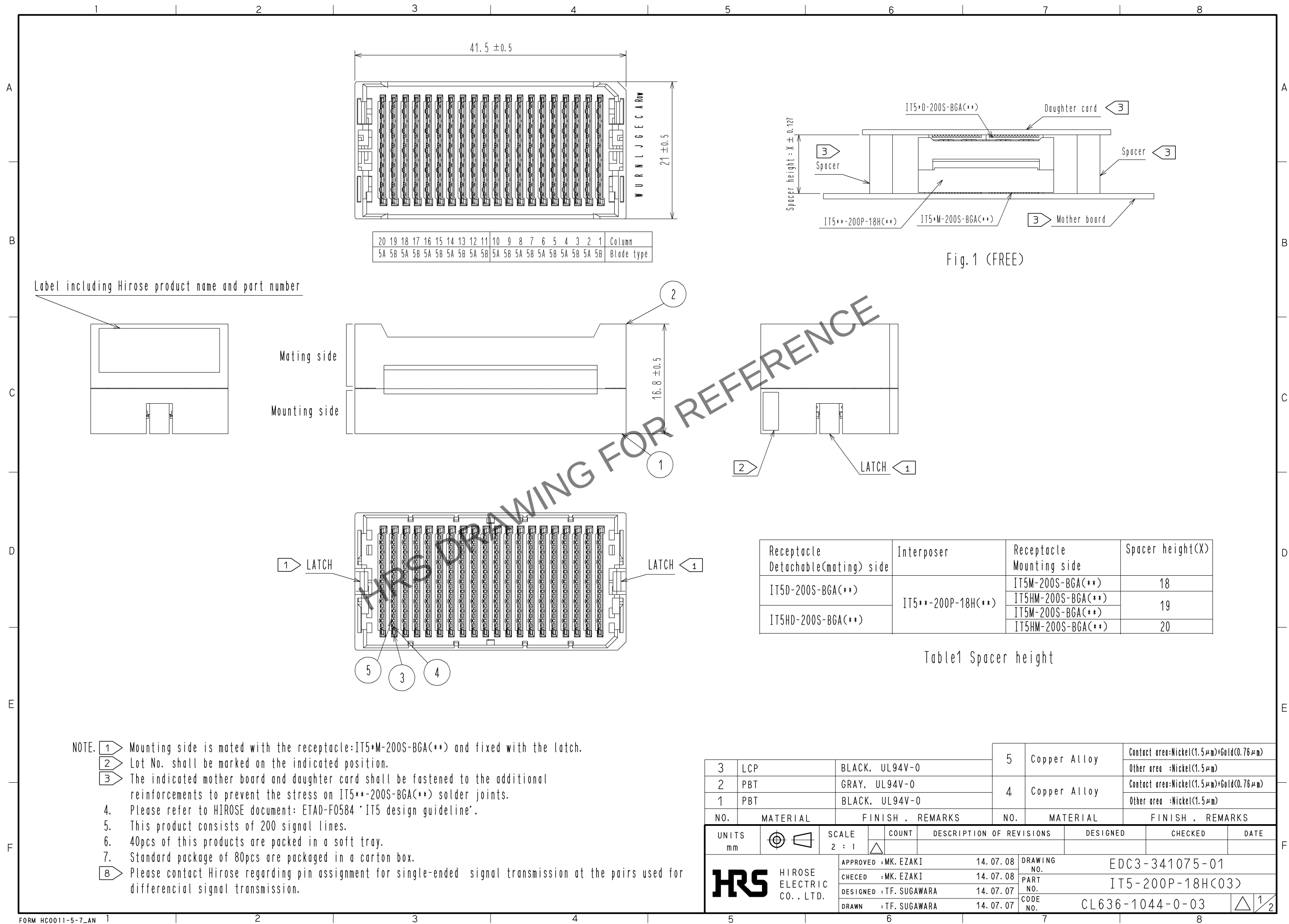




A
B
C
D
E
F

A
B
C
D
E
F

Wafer type		Wafer #		Column / Row	
IT5 wafer B (100 ohm)	1	1	1	X	
IT5 wafer A (100 ohm)	2	2	1	A	
IT5 wafer B (100 ohm)	3	3	1	B	
IT5 wafer A (100 ohm)	4	4	1	C	
IT5 wafer B (100 ohm)	5	5	1	D	
IT5 wafer A (100 ohm)	6	6	1	E	
IT5 wafer B (100 ohm)	7	7	1	F	
IT5 wafer A (100 ohm)	8	8	1	G	
IT5 wafer B (100 ohm)	9	9	1	H	
IT5 wafer A (100 ohm)	10	10	1	I	
IT5 wafer B (100 ohm)	11	11	1	J	
IT5 wafer A (100 ohm)	12	12	1	K	
IT5 wafer B (100 ohm)	13	13	1	L	
IT5 wafer A (100 ohm)	14	14	1	M	
IT5 wafer B (100 ohm)	15	15	1	N	
IT5 wafer A (100 ohm)	16	16	1	O	
IT5 wafer B (100 ohm)	17	17	1	P	
IT5 wafer A (100 ohm)	18	18	1	Q	
IT5 wafer B (100 ohm)	19	19	1	R	
IT5 wafer A (100 ohm)	20	20	1	S	
IT5 wafer B (100 ohm)	21	21	1	T	
IT5 wafer A (100 ohm)	22	22	1	U	
IT5 wafer B (100 ohm)	23	23	1	V	
IT5 wafer A (100 ohm)	24	24	1	W	
IT5 wafer B (100 ohm)	25	25	1	X	
IT5 wafer A (100 ohm)	26	26	1	Y	
IT5 wafer B (100 ohm)	27	27	1	Z	
IT5 wafer A (100 ohm)	28	28	1		
IT5 wafer B (100 ohm)	29	29	1		
IT5 wafer A (100 ohm)	30	30	1		
IT5 wafer B (100 ohm)	31	31	1		
IT5 wafer A (100 ohm)	32	32	1		
IT5 wafer B (100 ohm)	33	33	1		
IT5 wafer A (100 ohm)	34	34	1		
IT5 wafer B (100 ohm)	35	35	1		
IT5 wafer A (100 ohm)	36	36	1		
IT5 wafer B (100 ohm)	37	37	1		
IT5 wafer A (100 ohm)	38	38	1		
IT5 wafer B (100 ohm)	39	39	1		
IT5 wafer A (100 ohm)	40	40	1		

IT5•M-200S-BGA(**) Pin assignment footprint for mounting side

Wafer type		Wafer #		Column / Row	
IT5 wafer B (100 ohm)	1	1	1	Y	
IT5 wafer A (100 ohm)	2	2	1	W	
IT5 wafer B (100 ohm)	3	3	1	V	
IT5 wafer A (100 ohm)	4	4	1	U	
IT5 wafer B (100 ohm)	5	5	1	T	
IT5 wafer A (100 ohm)	6	6	1	R	
IT5 wafer B (100 ohm)	7	7	1	P	
IT5 wafer A (100 ohm)	8	8	1	N	
IT5 wafer B (100 ohm)	9	9	1	M	
IT5 wafer A (100 ohm)	10	10	1	L	
IT5 wafer B (100 ohm)	11	11	1	K	
IT5 wafer A (100 ohm)	12	12	1	J	
IT5 wafer B (100 ohm)	13	13	1	I	
IT5 wafer A (100 ohm)	14	14	1	H	
IT5 wafer B (100 ohm)	15	15	1	G	
IT5 wafer A (100 ohm)	16	16	1	F	
IT5 wafer B (100 ohm)	17	17	1	E	
IT5 wafer A (100 ohm)	18	18	1	D	
IT5 wafer B (100 ohm)	19	19	1	C	
IT5 wafer A (100 ohm)	20	20	1	B	
IT5 wafer B (100 ohm)	21	21	1	A	
IT5 wafer A (100 ohm)	22	22	1		
IT5 wafer B (100 ohm)	23	23	1		
IT5 wafer A (100 ohm)	24	24	1		
IT5 wafer B (100 ohm)	25	25	1		
IT5 wafer A (100 ohm)	26	26	1		
IT5 wafer B (100 ohm)	27	27	1		
IT5 wafer A (100 ohm)	28	28	1		
IT5 wafer B (100 ohm)	29	29	1		
IT5 wafer A (100 ohm)	30	30	1		
IT5 wafer B (100 ohm)	31	31	1		
IT5 wafer A (100 ohm)	32	32	1		
IT5 wafer B (100 ohm)	33	33	1		
IT5 wafer A (100 ohm)	34	34	1		
IT5 wafer B (100 ohm)	35	35	1		
IT5 wafer A (100 ohm)	36	36	1		
IT5 wafer B (100 ohm)	37	37	1		
IT5 wafer A (100 ohm)	38	38	1		
IT5 wafer B (100 ohm)	39	39	1		
IT5 wafer A (100 ohm)	40	40	1		

IT5•D-200S-BGA(**) Pin assignment footprint for mating side

Symbols

- G: GND, Power
P: Positive of differential pair
N: Negative of differential pair

8 Twisted pair used for differential

8 Non twisted pair used for differential

HRS	DRAWING NO.	EDC3-341075-01
	PART NO.	IT5-200P-18H(C03)
	CODE NO.	CL636-1044-0-03