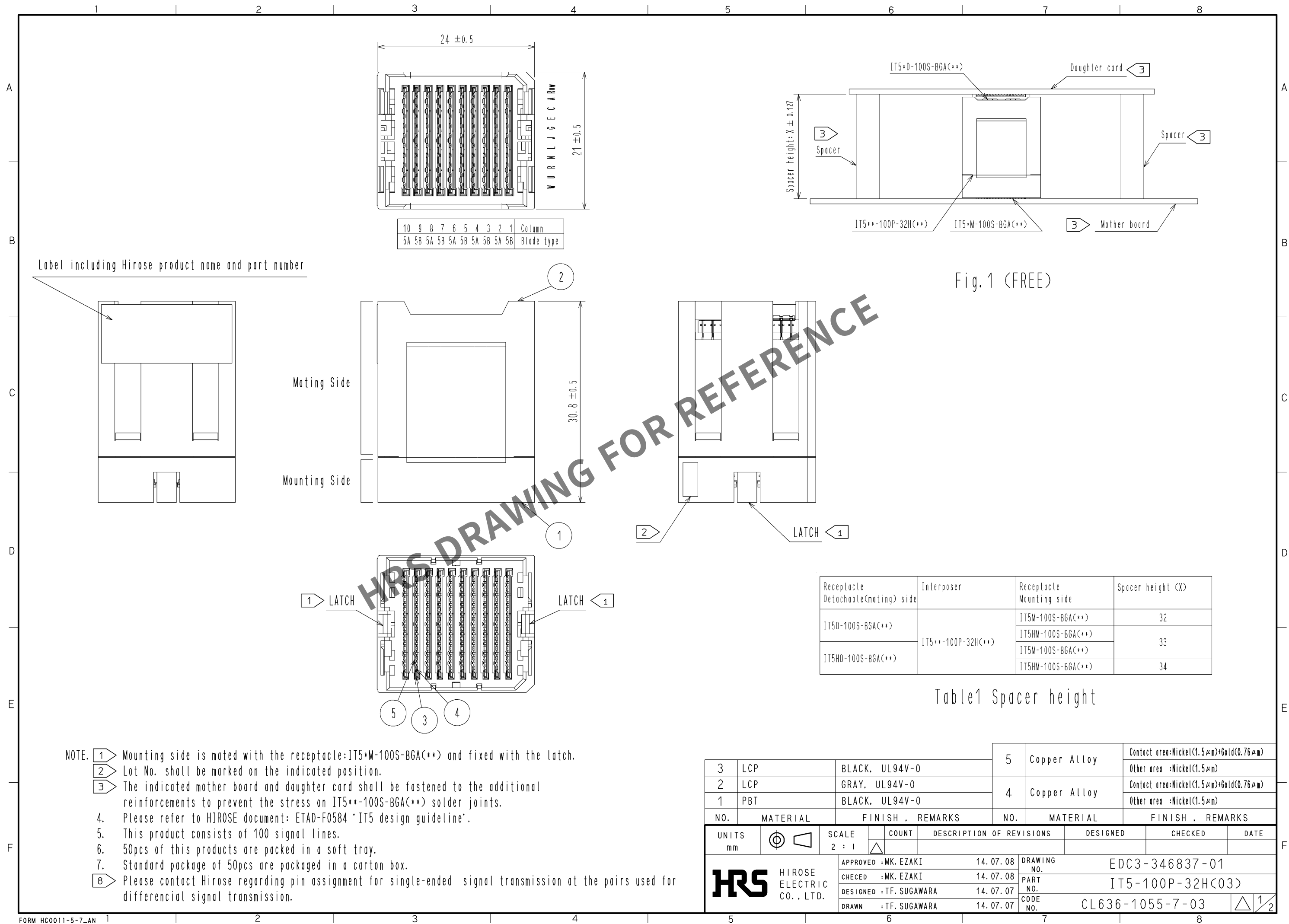




		Wafer type		Wafer #	
		Column	Row		
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4
ITS wafer A (100 ohm)	1	1	1	1	1
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	3	1	3	1	3
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ITS wafer B (100 ohm)	1	1	1	1	1
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	3	1	3	1	3
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ITS wafer A (100 ohm)	1	1	1	1	1
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ITS wafer B (100 ohm)	1	1	1	1	1
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ITS wafer A (100 ohm)	1	1	1	1	1
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	3	1	3	1	3
	4	1	4	1	4
ITS wafer B (100 ohm)	1	1	1	1	1
	2	1	2	1	2
	3	1	3	1	3
	4	1	4	1	4

IT5*M-100S-BGA(**) Pin assignment footprint for mounting side

[illegible]

IT5*D-100S-BGA(**) Pin assignment footprint for mating side

Symbols

G: GND, Power

P: Positive of differential pair

N: Negative of differential pair

8  Twisted pair used for differential

8

P
-
N

 Non twisted pair used for differential