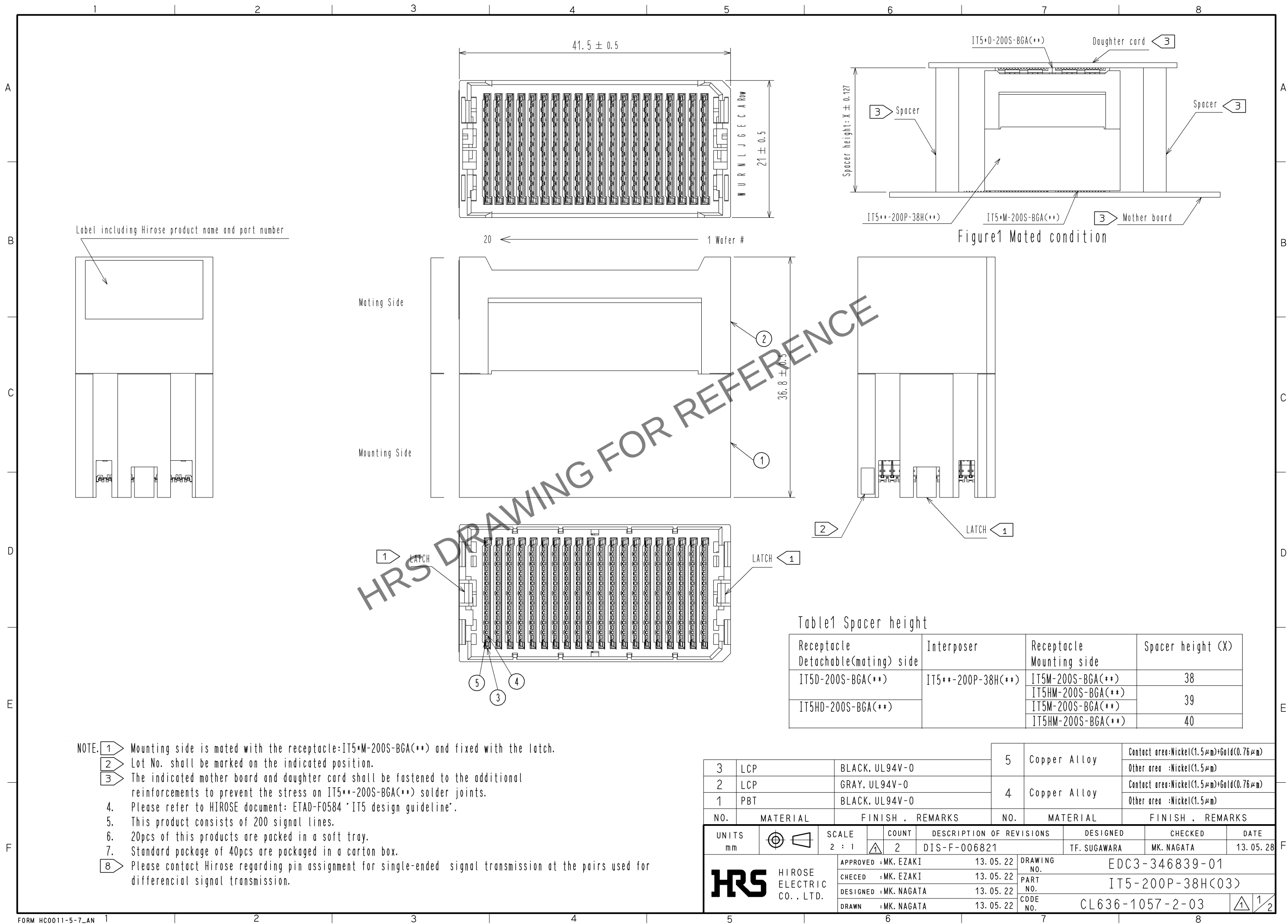




[illegible]

Pin connection
Wafer odd #
(Wafer B of IT5)

G: GND, Power
P: Positive of differential pair
N: Negative of differential pair

8  Non twisted pair used for differential

① IT5*D-200S-BGA(**) Pin assignment footprint for mating side