Applicab	olo etandard								
Applicat	ole standard				T_				
Rating	Operating Temperature Range		-55 to +105°C(Note1)		Stora Tem	age perature Range	-10 °C to +60°C (Note3)		
	Operating Humidity Range		20% to 80% (Note2)		Stora	age iidity Range	40% to 70% (Note3)		
Voltage Current			630 V AC/DC App			icable Connector	DF63-3S-3.96C		
			AWG #20 : 8A AWG #22 : 7A		Appl	icable Contact	DF63-1618PC * DF63-2022PC *		
<u> </u>		Rated Voltage				Overvoltage Category	IP-Degree		
UL,C-UL		600V AC/DC 300V AC/DC		See above		-	-		
TUV		300V AC	/DC	See above			IP00		
				Specificatio	ns				
	Item		nethod		Requirement	s	QT	AT	
Constru	ction								
General Examination		Visually and by measuring instrument.				According to drawing.			Х
Marking		Confirmed visually.							Х
Electric	Characteristics							•	•
Insulation Resistance		500 V DC.				1000 MΩ MIN.			_
Voltage Proof		1500 V AC for 1 min.				No flashover or breakdown.			_
Mechan	ical Characteris	stics							
Mechanical operation		30 times insertion and extraction				No damage, crack or looseness of parts.			_
Vibration		Frequency 10 to 55 Hz, single amplitude 0.75 mm, at							_
		10 cycles for 3 d	direction.						
time		Acceleration 490 m/s ² duration of pulse 11 ms at 3						Х	_
			imes for 3 directions.						
	mental Charact							X	
Damp Heat		Exposed at $40 \pm 2^{\circ}$, humidity 90 to 95 %, 96 h.				1.Insulation resistance: 500 MΩ MIN.			_
						2.No damage, crack or looseness of parts.			
Rapid Change Of Temperature		Temperature $-55^{\circ}\mathbb{C} \rightarrow +85^{\circ}\mathbb{C}$ Time $30 \text{min} \rightarrow 30 \text{min}$ Under 5 Cycles.				1.Insulation resistance: 1000 MΩ MIN. 2.No damage, crack or looseness of parts.			_
		(The transferring time of the tank is 2 to 3 MIN) (After leaving the room temperature for 1 to 2h.)							

Remarks
Note 1: Include the temperature rising by current.
Note 2: No condensing
Note 3: Apply to the condition of long term storage for unused products before PCB on board. After PCB on board, operating temperature

	Coun	nt	Description of revisions	Designed		Checked	Date	
Λ	1		DIS-H-00002332	MI. SAKIMURA		TS. FUKUSHIMA	16. 11. 29	
				Approved	KI. AKIYAMA	16. 04. 12		
				Checked	TS. FUKUSHIMA	16. 04. 11		
				Designed	YK. YAMAGUCHI	16. 04. 11		
Unles	s otherwi	ise spe	ecified, refer to IEC 60512.	Drawn	YK. YAMAGUCHI	16. 04. 11		
Note	QT:Qua	alificati	ion Test AT:Assurance Test X:Applicable Test	Drawing no.		ELC-361252-00-00		
Ч	र	Specification sheet		Part no.		DF63-3EP-3. 96C		
11.			Hirose electric co., ltd.	Code no.	CL680-0532-7-00		1/1	