Rating	Operating Temperature Range		-40 to +105 °C (Note 1) S		Storag	e Temperature Range	-10 °C to +60 °C (Note 3)		te 3)	
	Operating Humidity Range		20% to 80% (Note 2) S		Storag	e Humidity Range	40 % to 70 % (Note 3)			
	Voltage		630 V AC/DC A		Applic	able Connector	DF63-6S-3.96C			
	Current		AWG 16 : 10A A AWG 18 : 8A AWG 20 : 7A AWG 22 : 6A		Applic	able Contact	DF63-1618PC * DF63-2022PC *			
	•		Voltage	Rated Curre		Overvoltage Category	IP-De	gree		
	UL,C-UL		AC/DC	See above		-	- IP00			
	ΤÜV	300V	AC/DC	See above		П				
				Specification	ons					
	Item		Test method			Requirements		QT	AT	
Construc	ction									
General Ex	xamination	Visually and by measuring instrument.			Ad	According to drawing.			Х	
Marking		Confirmed visually.						Х	Х	
Electric (	Characteristics									
Insulation Resistance		500 V DC.	500 V DC.			1000 MΩ MIN.			-	
Voltage Proof		1500 V AC for 1 min.			N	No flashover or breakdown.			—	
Mechani	cal Characteri	stics								
		Frequency 10 to 55 Hz, single amplitude 0.75 mm, at 10 cycles for 3 direction.			at No	No damage, crack or looseness of parts.			—	
			Acceleration 490 m/s <sup>2</sup> duration of pulse 11 ms at 3 times for 3 directions.			No damage, crack or looseness of parts.			_	
Environr	nental Charact	eristics								
		Exposed at $40 \pm 2 \degree C$ , humidity 90 to 95 %, 96 h. (After leaving the room temperature for 1 to 2 h.)				1.Insulation resistance: 500 MΩ MIN. 2.No damage, crack or looseness of parts.			_	
Rapid Change Of Temperature		Temperature-55 °C $\rightarrow$ +85 °CTime30 min $\rightarrow$ 30 minUnder 5 Cycles.(The transferring time of the tank is 2 to 3 min)(After leaving the room temperature for 1 to 2 h.)				1.Insulation resistance: 1000 MΩ MIN. 2.No damage, crack or looseness of parts.				

Note 2: No condensing. Note 3: Apply to the condition of long term storage for unused products before mounted on PCB. After mounted on PCB board , operating temperature and humidity range is applied for interim storage during transportation.

	COUN	NT DESCRIPTION OF REVISIONS	DESIGNED		CHECKED	DATE	
$\underline{\Lambda}$	2	DIS-H-00004236 TS. KUMAZAWA		SZ. ONO		20180925	
				APPROVED	KI.AKIYAMA	20151026	
			CHECKED	TS. FUKUSHIMA	20151026		
			DESIGNED	YK. YAMAGUCHI	20151026		
Unles	s otherw	ise specified, refer to IEC 60512.	DRAWN	HK. HAYASHI	20151026		
Note	QT:Qua	alification Test AT:Assurance Test X:Applicable Te	st DRAWING	DRAWING NO.		ELC-365920-00-00	
н	ও	SPECIFICATION SHEET	PART NO.	DF63A-6EP-3.96C			
		HIROSE ELECTRIC CO., LTD.	CODE NO.	CL680-0558-0-00			

FORM HD0011-2-1