APPLICA	BLE STAND	DARD							
	OPERATING TEMPERATURE RANGE		-55 °C TO 125 °C(NO	TES 1)	STORAGE TEMPERAT	URE RANGE	-10 °C TO 60 °C (N	DTES 2	2)
RATING			50 V AC						
	CURRENT		0.3 A						
		1	SPEC	IFICAT	IONS			-	1
	EM		TEST METHOD			REQU	IREMENTS	QT	AT
								V	V
					ACCC	ACCORDING TO DRAWING.			X
								^	^
	IC CHARA		51165 C OR I ESS 1 kHz 1 mA		50 m(				1
						50 MQ MAX			_
									—
					NO FI	ING I LAGIIOVEN ON DREANDOWIN.			—
	OPERATION		ISTICS S INSERTIONS AND WITH		1 00				1
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE			. () CC ② NO	CONTACT RESISTANCE: 50 MM2 MAX.     2 NO DAMAGE CRACK AND LOOSENESS OF PARTS			_
						<ol> <li>NO ELECTRICAL DISCONTINUITY OF 1 μs.</li> </ol>			_
		0.75 mm, AT 2 h, FOR 3 DIRECTIONS.			<li>2 NO</li>	2 NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
		490 m/s <sup>2</sup> DURATION OF PULSE 11 ms AT 3 TIMES				1 NO ELECTRICAL DISCONTINUITY OF 1 $\mu$ s.			—
		FOR 3 DIRECTIONS.				DAMAGE, CRAC	CK AND LOOSENESS OF PARTS.		
ENVIRON RAPID CHA			1 = RISTICS	→15 TO 35	°C ① CO	NTACT RESIS			_
TEMPERATURE		TIME $30 \rightarrow 2 \text{ TO } 3 \rightarrow 30 \rightarrow 2 \text{ TO } 3 \text{ min}$			2 INS	(2) INSULATION RESISTANCE: 500 M $\Omega$ MIN.			
		UNDER 5 CYCLES.			3 NO	③ NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
DAMP HEAT (STEADY STATE)		EXPOSED AT 40 ± 2 °C, 90 TO 95 %, 96 h.			① CO ② INS	(1) CONTACT RESISTANCE: 50 m $\Omega$ MAX. (2) INSULATION RESISTANCE: 500 M $\Omega$ MIN.			_
					3 NO	③ NO DAMAGE, CRACK AND LOOSENESS OF PARTS.			
SULPHUR DIOXIDE		EXPOSED IN 25 PPM RH 75 % FOR 96 h. (TEST STANDARD: JEIDA-38)			① CO	① CONTACT RESISTANCE: 50 mΩ MAX. ② NO HEAVY CORROSION			—
HEAT RESISTANCE OF		[RECON	IMENDED TEMPERATURE PR		NO DE	FORMATION	OF CASE OF EXCESSIVE	X	_
		MAX250°C, 220°C FOR 60 SECONDS MAX. 《PREHEATING AREA》 150 TO 180°C 90~120 SECONDS. MAXIMUM TWICE ACTION IS ALLOWED UNDER THE SAME CONDITION. [RECOMMENDED MANUAL SOLDELING CONDITION ] SOLDERING IRON TEMPERATURE 350°C SOLDERING TIME : WITHIN 3 SECONDS.			THE N ]				
REMARKS NOTES1:INCL NOTES2:STO APPLY OPER UNLESS OTH	LUDING THE TE RAGEIS DEFINI ATION TEMPER	MPERATUR ED AS LON ATURE RA	RE RISE BY CURRENT. G-TERM STORAGE OF UNUSE NGE TO PRODUCTS MOUNTE ER TO JIS C 5402	D PRODUC D ON PCB V	TS. WITHOUT PO	WER SUPLLY.			
COUNT DESCRIPTION			DN OF REVISIONS DESIG				CHECKED	DA	TF
									<u> </u>
	I					APPROVE	D WR. FUKUCHI	2020	0721
						CHECKED	D TS. MIYAZAKI	2020	0721
						DESIGNE	D KT. KUSAKA	20200721	
						DRAWN	RN. I IDA	2020	0717
Note QT:Qualification Test AT:Assurance Test X:Applicable Test					DRAWI	RAWING NO. ELC-389315-5		1-01	
	S	SPECIFICATION SHEET			PART NO.	DF12	VC (4. 0) -30DP-0. 5V (51)		
	HIR	OSE EI	LECTRIC CO., LTD.	C	CODE NO.	CL53	CL537-0699-0-51		1/1