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PCB LAYOUT

PICK AND PLACE CAP

2.5 REF.
PICK AND PLACE AREA

- NOTE
- 1 CO-PLANARITY IS WITHIN 0.08.
 - 2 LOT No. SHALL BE INDICATED AT THE POSITION AS SHOWN.
 - 3 PLATING
CONTACT AREA : GOLD PLATE 0.75 μm MIN.
LEAD AREA : GOLD PLATE 0.05 μm MIN.
UNDER PLATING : NICKEL 2 μm MIN.
 - 4 PACKING
300 PCS PER TRAY.
 - 5 AS FOR PART No. 3, THE PERFORMANCE REMAINS GOOD EVEN IF THERE IS A RUBBING WOUND BY THE ASSEMBLY PROCESS. AND THE PERFORMANCE REMAINS GOOD EVEN IF THERE IS A DIFFERENCE OF THE COLOR OF PLATING WITH THE MANUFACTURING LOT.

2	LCP	BLACK	UL94V-0	5	PS	
1	COPPER ALLOY	3		4	POLYAMIDE RESIN	
NO.	MATERIAL	FINISH . REMARKS		3	STAINLESS STEEL	UNDER PLATING: NICKEL 0.2 μm MIN. OVER PLATING : TIN 1 μm MIN. LUBRICANT
NO.	MATERIAL	FINISH . REMARKS		NO.	MATERIAL	FINISH . REMARKS
UNITS mm		SCALE 5 : 1	COUNT 	DESCRIPTION OF REVISIONS		
DESIGNED			CHECKED			DATE
APPROVED : NM. NISHIMATSU			15.10.27	DRAWING NO.		
CHECKED : KN. ICHIKAWA			15.10.27	PART NO.		
DESIGNED : TS. ITO			15.10.27	CODE NO.		
DRAWN : AK. AKIYAMA			15.10.27	CL242-0051-0-30		
HRS			HIROSE ELECTRIC CO., LTD.	EDC-126723-30-00		
				ZX80-B-5P<30>		
				1		
				2		

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