DEV	001111	L DEGG	DIDTION OF DEL	(1010110	DV	OLIKE	DATE	DEV	LOCUNIT	DEO	ODIDTION OF DEVICIONS	I pv	OLUKB	Б.	
REV	COUNT	REVISED	RIPTION OF REV	ISIONS	BY JS.PARK	CHKD HJ.LEE	DATE 16.08.03	REV	COUNT 2	REVISE			CHKD HJ.LEE	16.1	TE
/ <u>2</u> \ / <u>3</u> \		- REVISED			JS.PARK	HJ.LEE	16.10.12	<u>/\$</u>	6	REVISE				17.1	
<u>/</u> 3\	_	- REVISED			JS.PARK	HJ.KIM	16.12.07	<u>/6\</u>	-	REVISE					
APPLICABLE STANDARD					JS.PARK HJ.KIM 16.12.07 — REVISED YG.KIM HJ.LEE 21.08.31 Universal Serial Bus Type—C Cable and Connector Specification Release 2.1 Universal Serial Bus Type—C Connectors and Cable Assemblies Compliance Document Revision 2.1b										
RATING				1.25A max. for each power pin (i.e. A1, A4, A9, A12, B1, B4, B5, B9, B12) 0.25A for the other pins											
VOLTAGE			20	20V AC/DC											
OPERATING CONDITION			-40	-40℃ ~ +105℃(INCLUDING TEMP. RISE), 95% RH MAX.(NON-CONDENSING)											
	STOF	RAGE C	ONDITION	-10	-10℃ ~ +60℃(WITH PACKING), 15% ~ 70% RH										
Para.	Para. Test Description				Test Procedure						Test Requirement			QT	ΑТ
1	Exa	minatio	n of product		A 364-18 sual inspection					١	No physical damage			0	0
Electi	rical R	equiren	nents							•					
2	2 Low Level Contact 1 Resistance 4			Measu 100m/ 4-wire resista	IA 364-23 Measure at 20mV max open circuit at 00mA(DC OR 1000Hz)wire measurement is required and the esistance of PCB termination shall be leducted from the reading.						40m $\Omega$ max initial for each contact. 50m $\Omega$ max after initial measurement.				ı
3	Dielectric Mo Withstanding Voltage co			Measu conditi	EIA 364-20					١	No distruptive discharge.				1
4	4 I Insulation Resistance I			1	A 364-21 00VDC with unmated and mated condition.				n. 1	100MΩ Min.				1	
Mech	Mechanical Requirements														
5	5 Insertion force				EIA 364-13 Measure at 12.5mm/minute min.					A	Initial: 5N to 20N After test: 5N to 20N (with virgin socket)				-
6	6 Extraction force				IA 364-13 Measure at 12.5mm/minute min.					A	Initial: 8N to 20N After test: 6N to 20N (with virgin socket)			0	ı
7					extration cycles 50 cycles per hour.			c	Low level contact resistance and dielectric withstanding voltage shall meet the spec after test.			0	1		
8	Vibration  EIA 364-28  Test Condition VII, Test Letter D  20-500 Hz random levels, 15minute each of 3 mutually perpendicular dire				C L	No physical damage and no discontinuity longer than 1 us. Low level contact resistance shall meet spec before and after the test.			0	1					
REMARKS						DR	4FT	DES	SIGN	CHECK APP	ROVAL	RELI	EAS	E	
						JS.P	ARK 8.03	JS.F	PARK 08.03				PT 8.3	7	
NOTE) (	QT : QL	IALIFICA	TION TEST, AT	: ASSUR	ANCE T	EST, O:	Applicat	le Test	1				I		
DWG N	0		31979-00		CL NO		6240				PART NO CX60-2	4S-L	JNIT		
TE LUDGOE KODEA CO. LED. DECULIO ODECUEIO ATION.															

Para.	Test Description	Test Procedure	Test Requirement	QT	ΑТ				
Environmental Requirements									
9	Temperature Life	EIA 364-17, Method A 105℃ without applied voltage for 120hours.	Low level contact resistance shall meet spec before and after the test.		_				
10	Cyclic Temperature and Humidity	EIA 364-31 25±3°C at 80±3% RH for 1 hour. 65±3°C at 50±3% RH for 1 hour. Thermal ramp: 0.5 hour Number of cycles: 24 cycles	Low level contact resistance shall meet spec before and after the test.	o	ı				
11	Thermal Shock	EIA 364-32, Test Condition I 10cycles -55°C and +105°C	No physical damage. Low level contact resistance shall meet spec before and after the test.	0	-				
12	Damp Heat (Steady State)	+85℃ and 85%RH for 120hours in mated condition.	No physical damage. Low level contact resistance shall meet spec before and after the test.	0	-				
13	EIA 364-52 Solderability Dwell in 245℃±5℃ of the solder bath 5sec.		Solder coverage shall be 95% min.of the immersed surfaces.		_				
14	Salt Spray	EIA 364-26 5% of NaCl in 35°C for 48hours	No corrosions that affect to connector operation. Low level contact resistance shall meet spec before and after the test.	0	1				

REMARKS

NOTE) QT: QUALIFICATION TEST, AT: ASSURANCE TEST, O: Applicable Test

DWG NO CL NO PART NO

HIROSE KOREA.CO.,LTD

Qualification Test Sequence Table											
Dozo	Took December	Test Group									
Para.	Test Description	Α	В	С	D	Е	F	G	Н		
1	Examination of product	1, 5	1, 13	1, 5	1, 5	1, 5	1, 5	1, 3	1, 5		
2	Low Level Contact Resistance	2, 4	2, 12	2, 4	2, 4	2, 4	2, 4		2, 4		
3	Dielectric Withstanding Voltage		3, 11								
4	Insulation Resistance		4, 10								
5	Insertion force		5, 9								
6	Extraction force		6, 8								
7	Durability		7								
8	Vibration	3									
9	Temperature Life			3							
10	Cyclic Temperature and Humidity				3						
11	Thermal Shock					3					
12	Damp Heat (Steady State)						3				
13	Solderability							2			
14	Salt Spray								3		

## REMARKS

1) Numbers in the table above indicate the sequence corresponding to each test group.

NOTE) QT: QUALIFICATION TEST, AT: ASSURANCE TEST, O: Applicable Test								
DWG NO	CL NO	PART NO						
ELC4-631979-00	CL 6240-0001-9	CX60-24S-UNIT						