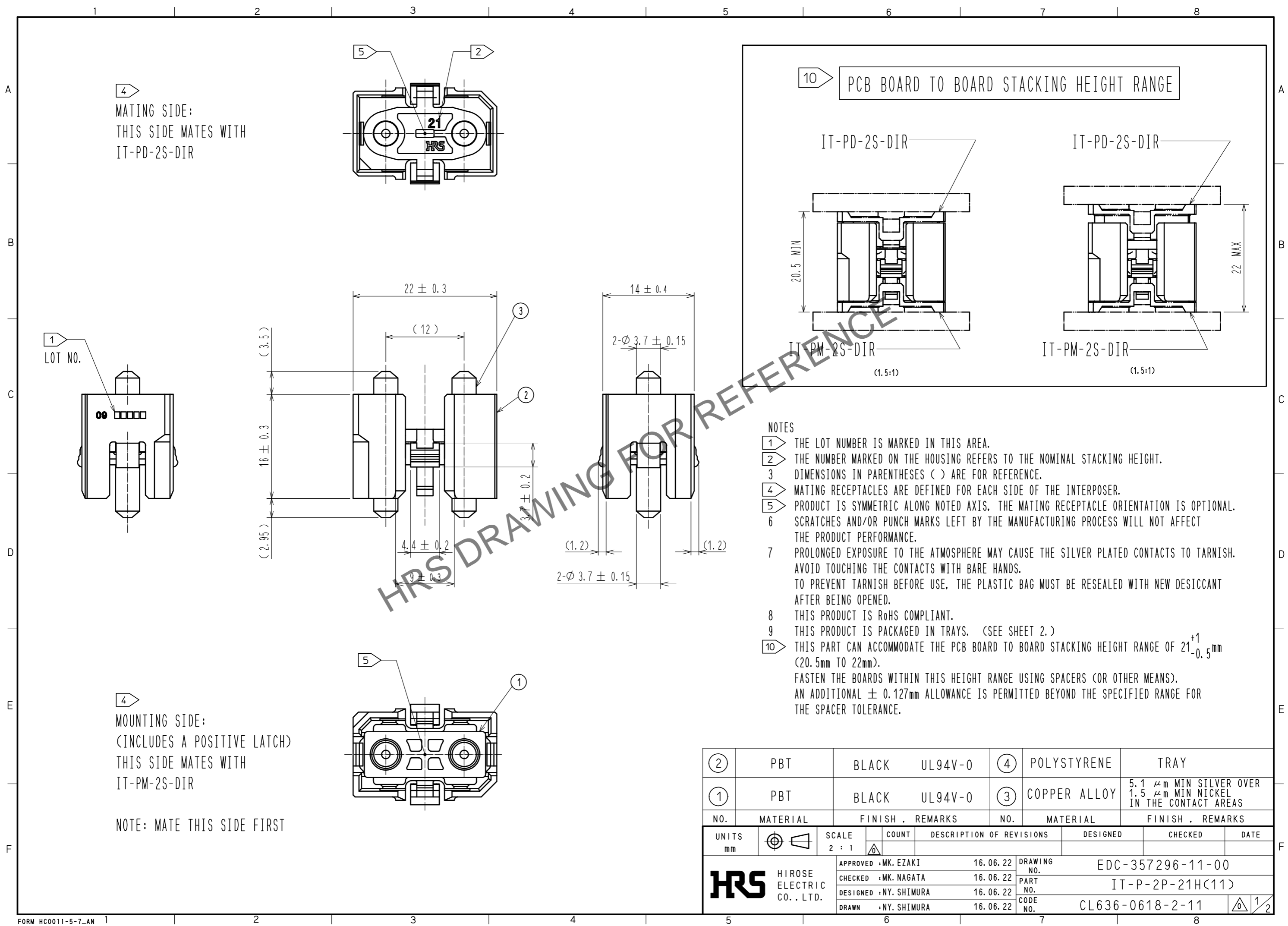
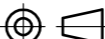







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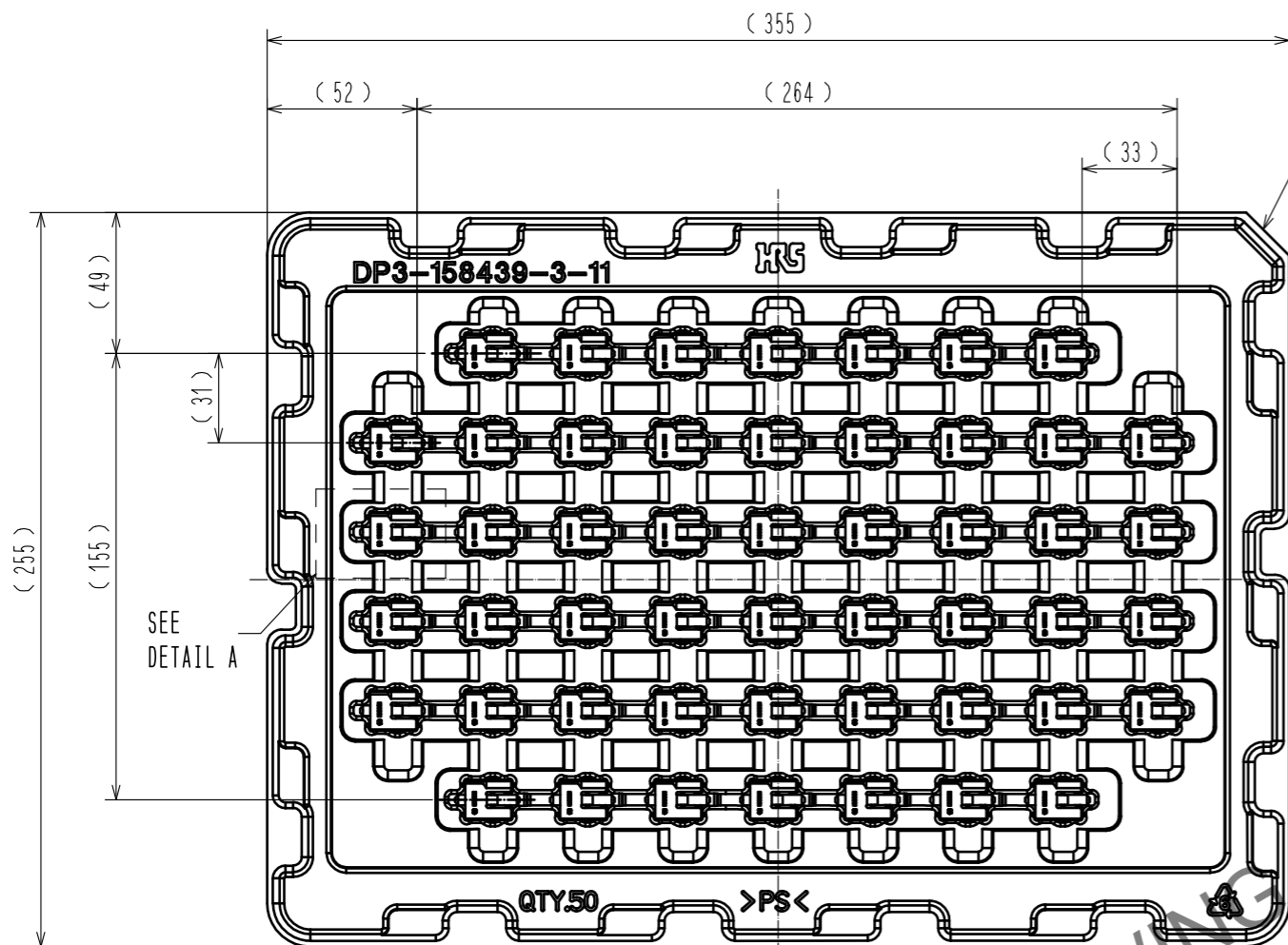


- NOTES
- 1 THE LOT NUMBER IS MARKED IN THIS AREA.
 - 2 THE NUMBER MARKED ON THE HOUSING REFERS TO THE NOMINAL STACKING HEIGHT.
 - 3 DIMENSIONS IN PARENTHESES () ARE FOR REFERENCE.
 - 4 MATING RECEPTACLES ARE DEFINED FOR EACH SIDE OF THE INTERPOSER.
 - 5 PRODUCT IS SYMMETRIC ALONG NOTED AXIS. THE MATING RECEPTACLE ORIENTATION IS OPTIONAL.
 - 6 SCRATCHES AND/OR PUNCH MARKS LEFT BY THE MANUFACTURING PROCESS WILL NOT AFFECT THE PRODUCT PERFORMANCE.
 - 7 PROLONGED EXPOSURE TO THE ATMOSPHERE MAY CAUSE THE SILVER PLATED CONTACTS TO TARNISH. AVOID TOUCHING THE CONTACTS WITH BARE HANDS. TO PREVENT TARNISH BEFORE USE, THE PLASTIC BAG MUST BE RESEALED WITH NEW DESICCANT AFTER BEING OPENED.
 - 8 THIS PRODUCT IS RoHS COMPLIANT.
 - 9 THIS PRODUCT IS PACKAGED IN TRAYS. (SEE SHEET 2.)
 - 10 THIS PART CAN ACCOMMODATE THE PCB BOARD TO BOARD STACKING HEIGHT RANGE OF 21⁺¹_{-0.5} mm (20.5mm TO 22mm). FASTEN THE BOARDS WITHIN THIS HEIGHT RANGE USING SPACERS (OR OTHER MEANS). AN ADDITIONAL ± 0.127mm ALLOWANCE IS PERMITTED BEYOND THE SPECIFIED RANGE FOR THE SPACER TOLERANCE.

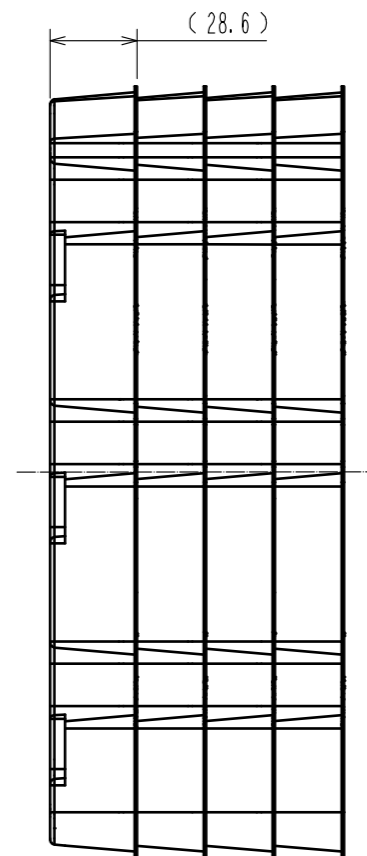
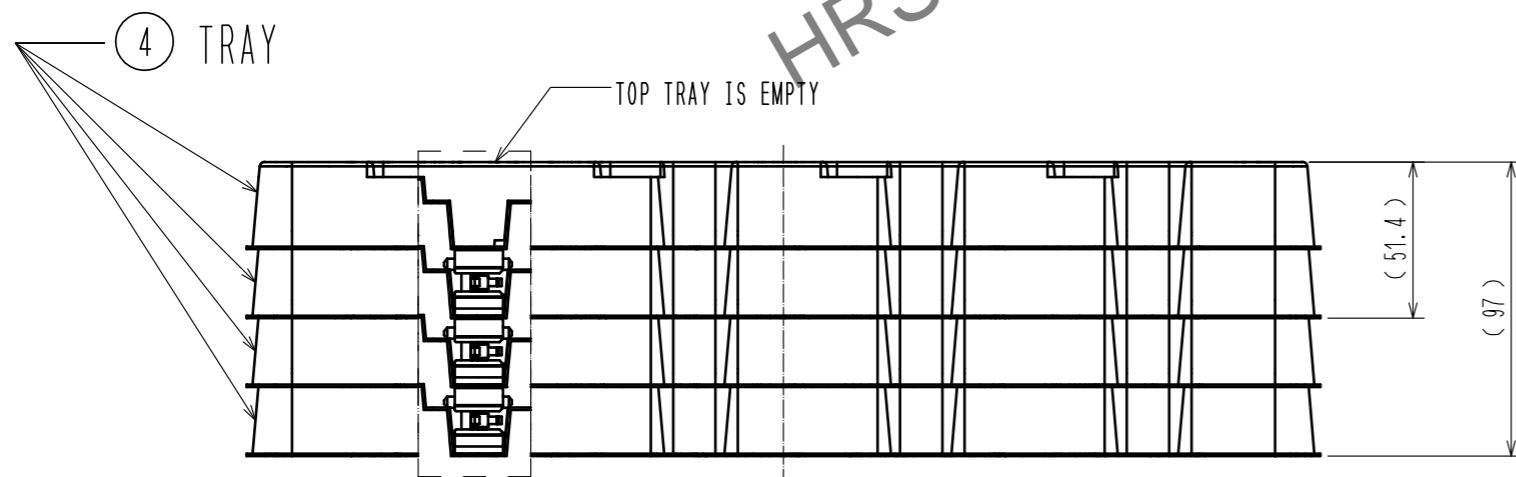
②	PBT	BLACK	UL94V-0	④	POLYSTYRENE	TRAY		
①	PBT	BLACK	UL94V-0	③	COPPER ALLOY	5.1 μm MIN SILVER OVER 1.5 μm MIN NICKEL IN THE CONTACT AREAS		
NO.	MATERIAL	FINISH , REMARKS		NO.	MATERIAL	FINISH , REMARKS		
UNITS mm		SCALE 2 : 1	COUNT 	DESCRIPTION OF REVISIONS		DESIGNED	CHECKED	DATE
 HIROSE ELECTRIC CO., LTD.		APPROVED .MK. EZAKI		16.06.22	DRAWING NO. EDC-357296-11-00			
		CHECKED .MK. NAGATA		16.06.22	PART NO. IT-P-2P-21HC(11)			
		DESIGNED .NY. SHIMURA		16.06.22	CODE NO. CL636-0618-2-11			
		DRAWN .NY. SHIMURA		16.06.22				
								

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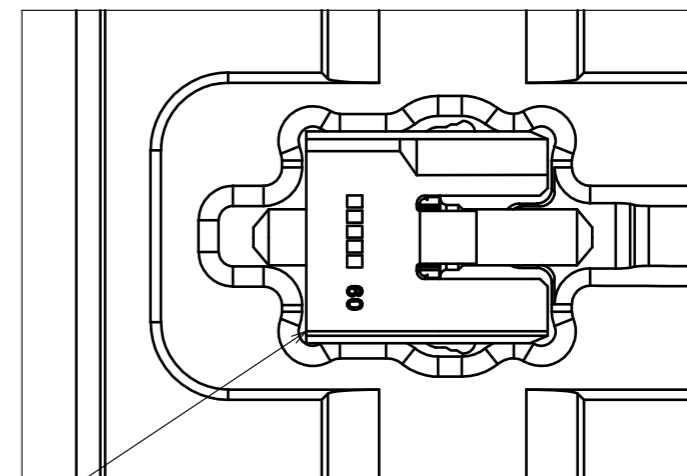
TRAY PACKAGING DIMENSIONS



(TOP TRAY REMOVED TO SHOW PRODUCT)



DETAIL A
(2:1)



THE CONNECTOR IS PLACED IN THE POCKET OF THE TRAY AS SHOWN.

SCALE 2:5

HRS	DRAWING NO.	EDC-357296-11-00
	PART NO.	IT-P-2P-21H(11)
	CODE NO.	CL636-0618-2-11
		2/2