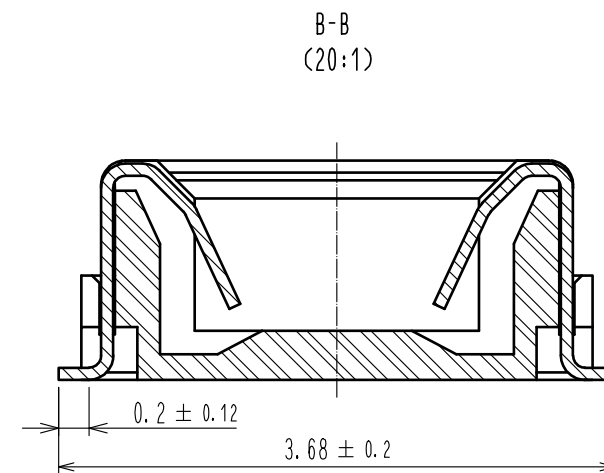
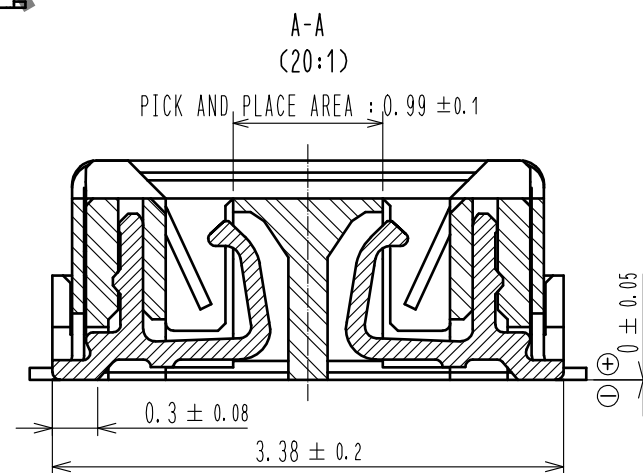
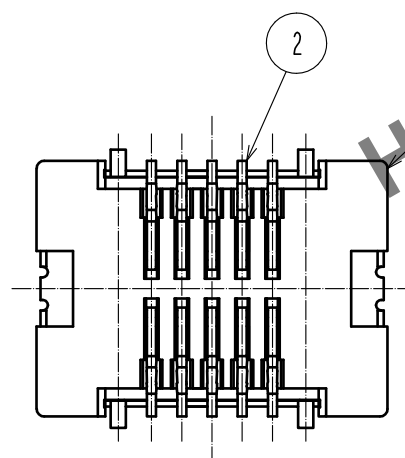
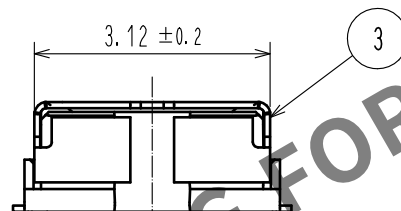
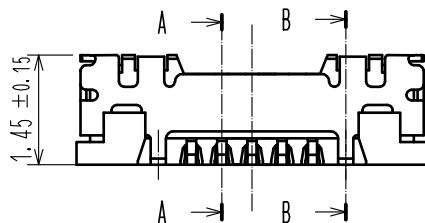
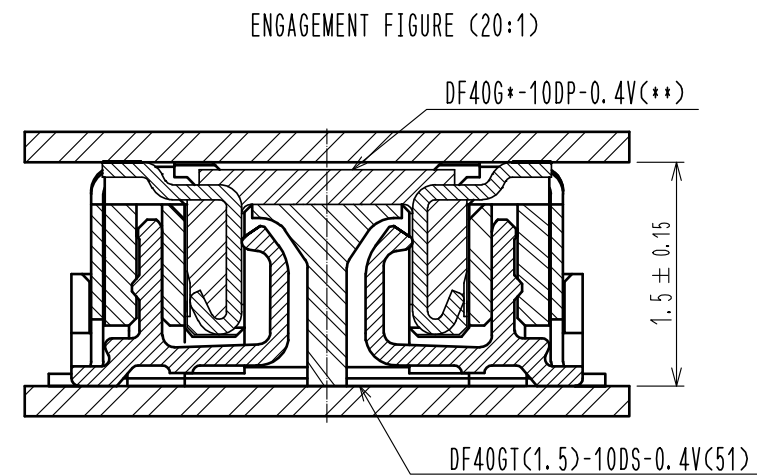
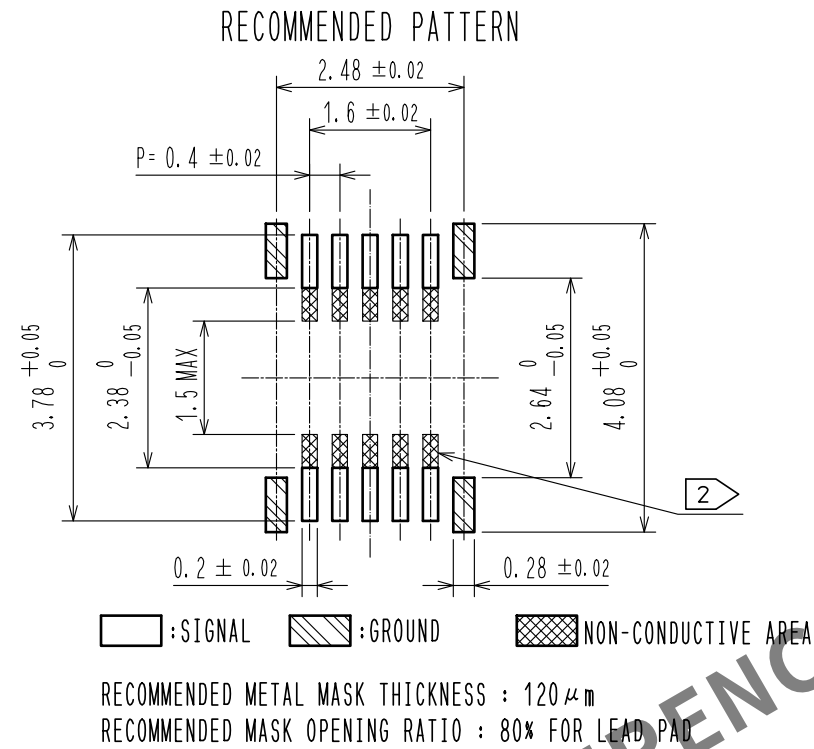
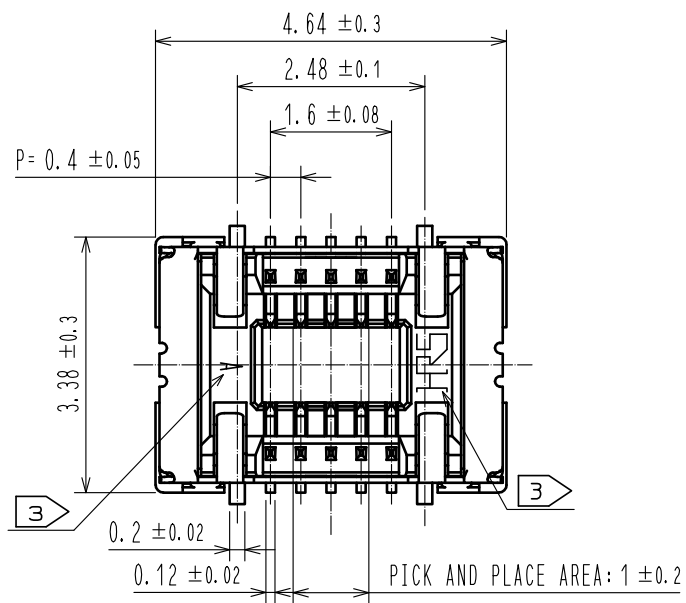




NOTE 1. ALL LEADS CO-PLANARITY SHALL BE 0.1 MAX.
 2  IS NO PATTERN AREA OTHER THAN THE SAME CIRCUIT.
 3 HRS MARK AND CAV NO. ARE INDICATED IN APPROX. POSITION SHOWN.

3	COPPER ALLOY	GOLD 0.05#m MIN UNDERPLATING:NICKEL 1#m MIN	7	PS	CLEAR, REINFORCEMENT COLLAR
2	COPPER ALLOY	GOLD 0.05#m MIN UNDERPLATING:NICKEL 1#m MIN	6	PS	BLACK, PLASTIC REEL
			5	POLYESTER	CLEAR, COVER TAPE
1	LCP	UL94V-0, BLACK	4	PS	CLEAR(EMBOSSD CARRIER TAPE)
NO.	MATERIAL	FINISH , REMARKS	NO.	MATERIAL	FINISH , REMARKS

UNITS mm		SCALE 10 : 1	COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
			0				
 HIROSE ELECTRIC CO., LTD.	APPROVED :WR. FUKUCHI		20201222	DRAWING NO. EDC-384225-51-01			
	CHECED :YK. SATAKE		20201222	PART NO. DF40GT(1.5)-10DS-0.4V(51)			
	DESIGNED :PAN YIWEI		20201221	CODE NO.			
	DRAWN :PAN YIWEI		20201221	CL0684-4248-0-51			

