	le standard				Storago							
	Operating Temperature range Operating Humidity range		-40 °C to +85°C (Note1) 20% to 80% (Note2)		Storage Temperature range			-10 °C to +60°C (Note3)			,	
Rating					Storage Humidity rang Applicable	e	40	40% to 70% (N			Note3)	
	Voltage		100V AC/DC AWG28 : 1.5A		Connector	Connector		DF52#-6S-0.8H				
	Current		AWG28 : 1.3A AWG30 : 1.2A AWG32 : 0.8A		Applicable contact			DF52-2832PCF				
				cificatio	ons							
I	ltem		Test method				Requireme	ents			QT	A
Construction					· · · · · · · · · · · · · · · · · · ·							
General exa	amination		y and by measuring instrume	nt.	Accord	ing to dra	wing.				X	
Marking	horostoriot		ned visually.								Х	
nsulation resi	characterist	100 V D	C.		100 MΩ	MIN.					Х	
Voltage proof		300 V AC for 1 min.			No flash	over or bre	akdown.				Х	
Vechani	cal charact	oriatia	•									
Mechanical			S insertion and extraction.		No dam	age, crack	or looseness	s of parts			Х	<u> </u>
Vibration		Frequency 10 to 55 Hz, single amplitude				-	or looseness				X	
Shock		0.75 mm, at 10 cycles for 3 direction. 490 m/s ² duration of pulse 11 ms at 3 times each for				age, crack	or looseness	s of parts			Х	
		3 both a	xial directions.			0		•			~	
	ental charac		CS d at 40 ± 2°C,90 to 95 %, 96 h.		Dinsula	tion regista	nce: 100 MO	MIN			V	
Damp heat (Steady state)			aving the room temperature for $1 - \frac{1}{2}$		-	 ①Insulation resistance: 100 MΩ MIN. ②No damage, crack or looseness of parts. 					Х	
Rapid change of temperature		Temperature -55°C→ +85°C Time $30min \rightarrow 30min$ Under 5 cycles.			-	①Insulation resistance: 100 MΩ MIN. ②No damage, crack or looseness of parts.				Х		
		Under 5	cycles.									
Note 2: No co Note 3: Apply	to the condition o	(The tra (After lea e rising by f long ter	nsferring time of the tank is 2 - 3 n aving the room temperature for 1 - 2h	n.) Fore mounted o		ransportatic	ın.					
lote 2: No co lote 3: Apply	ndensing to the condition o	(The tra (After lea e rising by f long ter	nsferring time of the tank is 2 - 3 n aving the room temperature for 1 - 2t r current. m storage for unused products bef	n.) Fore mounted o		ransportatio	n.					
lote 2: No co lote 3: Apply fter mounted	ndensing to the condition of d on PCB, operation	(The tra (After lea e rising by f long ter on tempe	nsferring time of the tank is 2 - 3 n aving the room temperature for 1 - 2t r current. m storage for unused products bef	ore mounted o ad for interim st		ransportatio		Checked	1		Da	l
lote 2: No co lote 3: Apply ffer mounted	ndensing to the condition of d on PCB, operation	(The tra (After lea e rising by f long ter on tempe	nsferring time of the tank is 2 - 3 n aving the room temperature for 1 - 2t r current. m storage for unused products bef rature and humidity range is applie	ore mounted o ad for interim st	orage during tr		C	Checked HS. 0k				
lote 2: No co lote 3: Apply fifer mounted	ndensing to the condition of d on PCB, operation	(The tra (After lea e rising by f long ter on tempe	nsferring time of the tank is 2 - 3 n aving the room temperature for 1 - 2t r current. m storage for unused products bef rature and humidity range is applie	ore mounted o ad for interim st	orage during tr	Approv	C		(AWA		<u>Da</u> <u>17. 1(</u> 17. 1)	0.
Cour Cour Cour	ndensing to the condition of d on PCB, operation nt	(The tra (After lea e rising by f long ter on tempe	iption of revisions	ore mounted o ad for interim st	orage during tr	Approve	C ed 1	HS. OK	(AWA JSHIMA		17. 10	0. 0.
Cour Cour	ndensing to the condition of d on PCB, operation of the condition of d on PCB, operation nt	(The tra (After lea e rising by f long ter on tempe Descr	iption of revisions er to IEC 60512.	ore mounted o ad for interim st	Designed	Approve Checke Designe Drawr	C ed ed n	HS. OK TS. FUKL HK. HAY HK. HAY	(AWA JSHIMA (ASHI (ASHI		17. 10 17. 10 17. 10 17. 10	0. 0. 0.
Lote 2: No co lote 3: Apply fifter mounted Remarks Jnless oth Lote QT:C	ndensing to the condition of d on PCB, operation of the condition of d on PCB, operation nt	(The tra (After lea e rising by f long ter on tempe Descr	iption of revisions	e Test	Designed	Approve Checke Designe Drawr	ed 1 ed 1 ed 2 h E	HS. OK TS. FUKL HK. HAY HK. HAY ELC–3	(AWA JSHIMA (ASHI (ASHI (ASHI 5 64 8		17. 10 17. 10 17. 10 17. 10	0. 0. 0.
Iote 2: No co Iote 3: Apply Ifter mounted Cour Remarks Jnless oth	ndensing to the condition of d on PCB, operation of the condition of d on PCB, operation nt nt nerwise specific Qualification Tes	(The tra (After lea e rising by f long ter on tempe Descr ied, refi st AT:/ Spe	iption of revisions er to IEC 60512.	ore mounted o ad for interim st	Designed	Approve Checke Designe Drawr g No.	ed 1 ed 1 ed 2 h E	HS. OK TS. FUKL HK. HAY HK. HAY ELC-3 52-6P	(AWA JSHIMA (ASHI (ASHI 5648 2-0.8	C	17. 10 17. 10 17. 10 17. 10)-00	0. 0. 0.