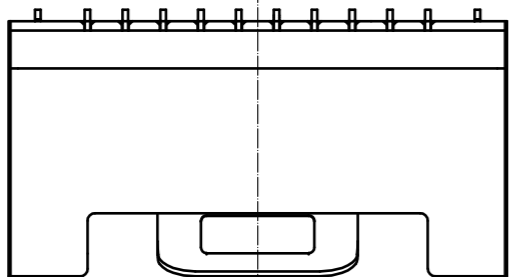
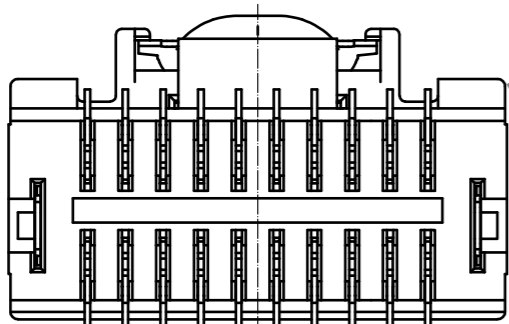
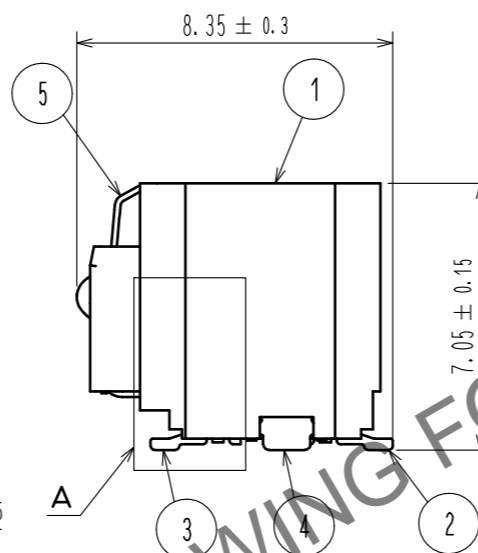
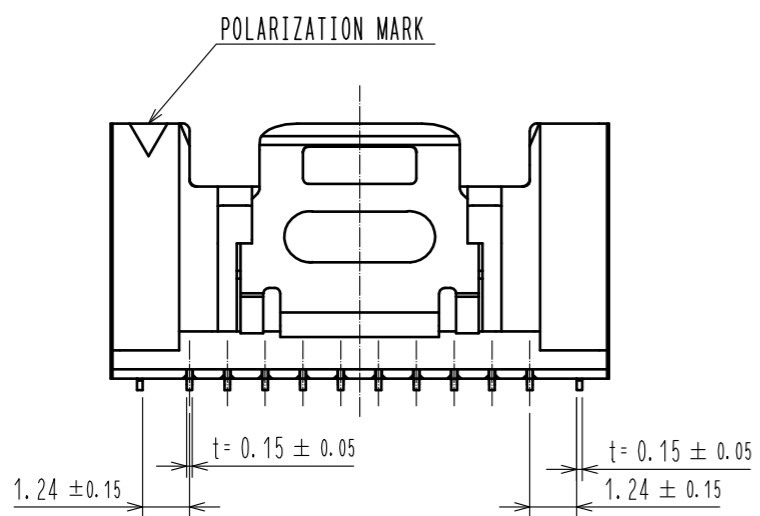
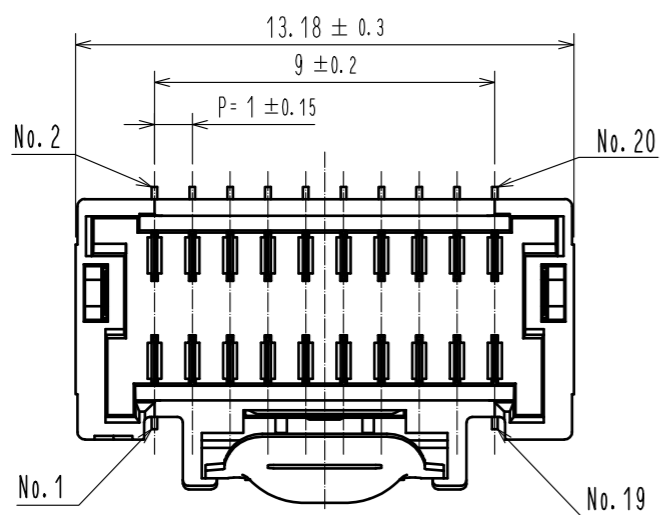
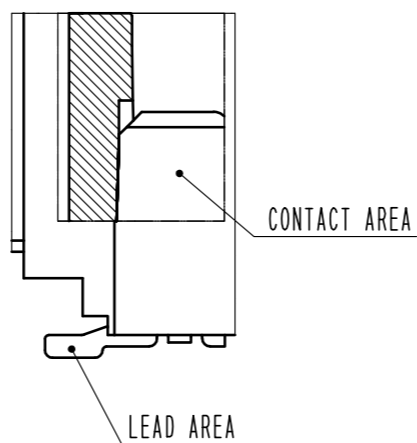


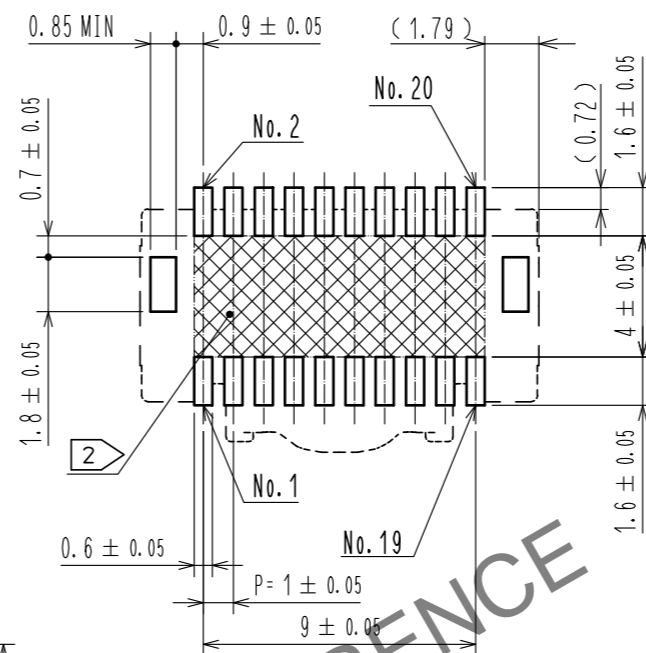
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A (10:1)

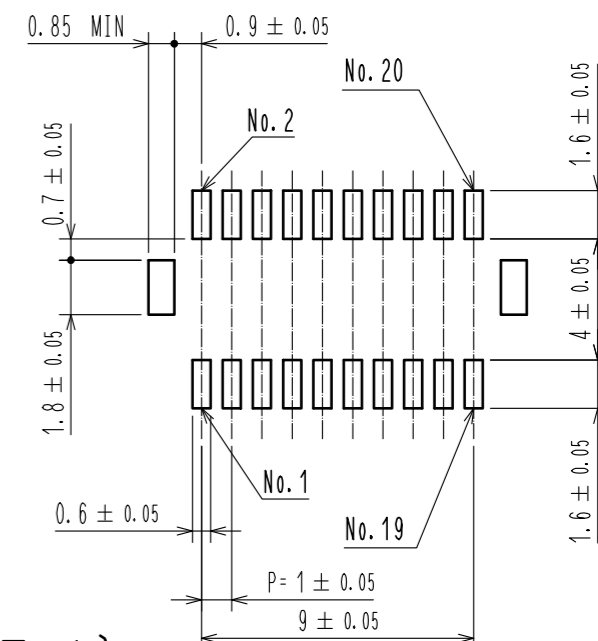


RECOMMENDED PCB LAYOUT
 (MOUNTING SURFACE SIDE) (FREE)

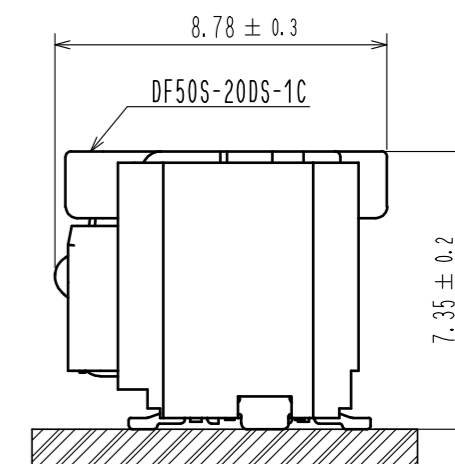
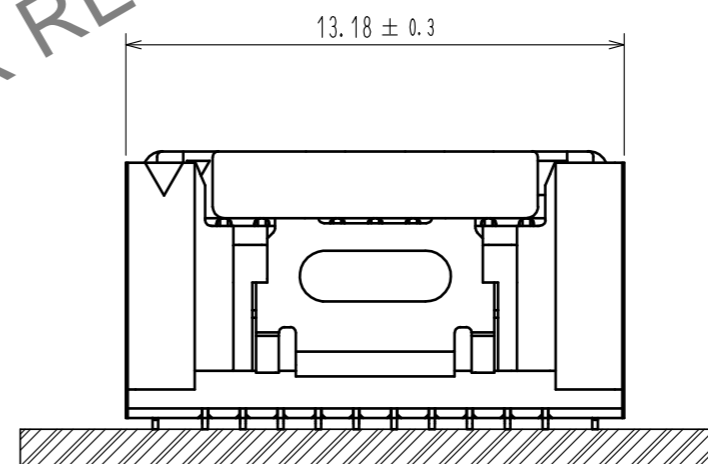


RECOMMENDED METAL MASK(FREE)

THICKNESS : 0.1mm



MATING FIGURE (5:1)



- NOTES 1. LEAD CO-PLANARITY SHALL BE 0.1 mm MAX.
 2. [Hatched Area] AREA INDICATED MUST BE FREE OF CONDUCTIVE TRACES OR THE CONDUCTIVE TRACES MUST BE COVERED BY RESIST FILM.
 3. PLEASE CONSULT HIROSE WHEN THE PRODUCT IS MOUNTED TO FPC.

3	BRASS	CONTACT AREA : Au PLATING 0.05µm MIN. LEAD AREA : Au PLATING 0.05µm MIN. UNDERPLATING : Ni PLATING 1µm MIN.	9	POLYAMIDE	VACUUM CAP
2	BRASS	CONTACT AREA : Au PLATING 0.05µm MIN. LEAD AREA : Au PLATING 0.05µm MIN. UNDERPLATING : Ni PLATING 1µm MIN.	8	PS	REEL, BLACK
1	LCP	BLACK, UL94V-0	7	POLYESTER	CLEAR
			6	PS	CLEAR
			5	STAINLESS STEEL	-
			4	BRASS	SURFACE : Sn PLATING 1µm MIN. UNDERPLATING : Ni PLATING 0.5µm MIN.
NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS

UNITS mm		SCALE FREE	COUNT 1	DESCRIPTION OF REVISIONS DIS-H-00005059	DESIGNED HT. SATO	CHECKED HS. OKAWA	DATE 20190711
HRS HIROSE ELECTRIC CO., LTD.		APPROVED : OM. MIYAMOTO 20120131	DRAWING NO. EDC-339519-52-02				
		CHECKED : OM. MIYAMOTO 20120131	PART NO. DF50-20DP-1V(52)				
		DESIGNED : TT. OHSAKO 20120131	CODE NO. CL665-0019-3-52				
		DRAWN : TT. OHSAKO 20120131					

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